

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1290	correct\$4 near4 (erroneous or error\$1 or defect\$2 or fault\$1 or failed) near4 (bit\$1 or byte\$1 or data or symbol\$1 or signal\$1) near4 sector\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/25 09:43
L2	1371	(ECC or correct\$4) near4 (erroneous or error\$1 or defect\$2 or fault\$1 or failed) near4 (bit\$1 or byte\$1 or data or symbol\$1 or signal\$1) near4 sector\$1	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/25 09:44
L4	31	((ECC or (ECC near3 correct\$3)) near4 (CRC or (cyclic adj2 redundancy)) near4 (bit\$1 or byte\$1 or data)) and ((failed or defected or erroneous) near4 sector\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/25 09:50
L5	36	((ECC or (ECC near3 correct\$3)) near10 (CRC or (cyclic adj2 redundancy)) near10 (bit\$1 or byte\$1 or data)) and ((failed or defected or erroneous) near4 sector\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/25 10:07
L6	86	(first near3 (ECC or (ECC near3 correct\$3))) and (second near3 (ECC or (ECC near3 correct\$3))) and (third near3 (ECC or (ECC near3 correct\$3)))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/25 09:58
L7	3	(first near3 (ECC or (ECC near3 correct\$3))) and (second near3 (ECC or (ECC near3 correct\$3))) and (third near3 (ECC or (ECC near3 correct\$3))) and ((failed or defected or erroneous) near2 sector\$1)	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/25 10:06
L8	17	(first near3 (ECC or (ECC near3 correct\$3))) and (second near3 (ECC or (ECC near3 correct\$3))) and (third near3 (ECC or (ECC near3 correct\$3))) and ((failed or defected or erroneous) near2 (data or sector\$1))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/25 09:59
L9	0	(first near3 (ECC or (ECC near3 correct\$3))) and (second near3 (ECC or (ECC near3 correct\$3))) and (third near3 (ECC or (ECC near3 correct\$3))) and ((failed or defected or erroneous) near2 (data or sector\$1)) and (LBM or ("long block membership"))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/25 10:05

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L10	6	((failed or defected or erroneous) near2 (data or sector\$1)) and (LBM or ("long block membership"))	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/25 10:01
L11	8	((failed or defected or erroneous) near2 (data or sector\$1)) and (LBM or ("long block membership"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:01
L12	0	(first near3 (ECC or (ECC near3 correct\$3))) and (second near3 (ECC or (ECC near3 correct\$3))) and (third near3 (ECC or (ECC near3 correct\$3))) and ((failed or defected or erroneous) near2 (data or sector\$1)) and (LBM or ("long block membership"))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:05
L13	4	(first near3 (ECC or (ECC near3 correct\$3))) and (second near3 (ECC or (ECC near3 correct\$3))) and (third near3 (ECC or (ECC near3 correct\$3))) and ((failed or defected or erroneous) near2 sector\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:19
L14	4	(first near3 (ECC or (ECC near3 correct\$3))) and (second near3 (ECC or (ECC near3 correct\$3))) and (third near3 (ECC or (ECC near3 correct\$3))) and ((failed or defected or erroneous) near2 sector\$1) and control\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:06
L15	35	((ECC or (ECC near3 correct\$3)) near10 (CRC or (cyclic adj2 redundancy)) near10 (bit\$1 or byte\$1 or data)) and ((failed or defected or erroneous) near4 sector\$1) and control\$4	USPAT; EPO; JPO; DERWENT	OR	OFF	2006/09/25 10:07
L16	1	(first near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1)) and (second near3 (ECC or (ECC near3 correct\$3))) and (third near3 (ECC or (ECC near3 correct\$3))) and ((failed or defected or erroneous) near2 sector\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:20

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L17	0	(first near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1)) and (second near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1)) and (third near3 (ECC or (ECC near3 correct\$3)) near4 (trial\$1 or attempt\$1)) and ((failed or defected or erroneous) near10 sector\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:21
L18	0	(first near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1)) and (second near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1)) and (third near3 (ECC or (ECC near3 correct\$3)) near4 (trial\$1 or attempt\$1)) and sector\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:21
L19	0	(first near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1)) and (second near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1)) and (third near3 (ECC or (ECC near3 correct\$3)) near4 (trial\$1 or attempt\$1)) and sector\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:21
L20	0	(first near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1)) and (second near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1)) and (third near3 (ECC or (ECC near3 correct\$3)) near4 (trial\$1 or attempt\$1)) and decod\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:22
L21	2	(first near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1)) and (second near3 (ECC or (ECC near3 correct\$3)) near3 (trial\$1 or attempt\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:30
L22	0	(add\$3 near10 (long adj2 (codeword or (block adj2 (bit\$1 or byte\$1)))) and ECC and CRC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:32
L23	5	(long adj2 (codeword or (block adj2 (bit\$1 or byte\$1)))) and ECC and CRC	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/25 10:32


**PALM INTRANET**

 Day : Monday  
 Date: 9/25/2006  
 Time: 10:59:52

**Inventor Name Search Result**

Your Search was:

Last Name = HASSNER

First Name = MARTIN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">06283758</a>	<a href="#">4413251</a>	150	07/16/1981	METHOD AND APPARATUS FOR GENERATING A NOISELESS SLIDING BLOCK CODE FOR A (1,7) CHANNEL WITH RATE 2/3	HASSNER, MARTIN
<a href="#">06336478</a>	<a href="#">4463344</a>	150	12/31/1981	METHOD AND APPARATUS FOR GENERATING A NOISELESS SLIDING BLOCK CODE FOR A (2,7) CHANNEL WITH RATE 1/2	HASSNER, MARTIN
<a href="#">06666842</a>	<a href="#">4609907</a>	150	10/31/1984	DUAL CHANNEL PARTIAL RESPONSE SYSTEM	HASSNER, MARTIN
<a href="#">07644160</a>	Not Issued	166	01/22/1991	MODULAR IMPLEMENTATION FOR A PARALLELIZED KEY EQUATION SOLVER FOR LINEAR ALGEBRAIC CODES	HASSNER, MARTIN
<a href="#">08127465</a>	<a href="#">5428628</a>	150	09/27/1993	MODULAR IMPLEMENTATION FOR A PARALLELIZED KEY EQUATION SOLVER FOR LINEAR ALGEBRAIC CODES	HASSNER, MARTIN
<a href="#">08668887</a>	<a href="#">5748023</a>	250	06/24/1996	INTEGRATOR WITH OUTPUT-COMPENSATING CAPABILITY	HASSNER, MARTIN
<a href="#">08838375</a>	<a href="#">5942005</a>	150	04/08/1997	METHOD AND MEANS FOR COMPUTATIONALLY EFFICIENT ERROR AND ERASURE CORRECTION IN LINEAR CYCLIC CODES	HASSNER, MARTIN
<a href="#">08971796</a>	<a href="#">5946328</a>	150	11/17/1997	METHOD AND MEANS FOR EFFICIENT ERROR DETECTION AND CORRECTION IN LONG BYTE STRINGS USING INTEGRATED INTERLEAVED REED-SOLOMON CODEWORDS	HASSNER, MARTIN
<a href="#">09667260</a>	<a href="#">6344807</a>	150	09/22/2000	Packet-frame generator for creating an	HASSNER,

				encoded packet frame and method thereof	MARTIN
<u>10817421</u>	Not Issued	40	04/02/2004	Techniques for detecting and correcting errors using multiple interleave erasure pointers	HASSNER, MARTIN
<u>11058596</u>	Not Issued	30	02/14/2005	Techniques for performing reduced complexity galois field arithmetic for correcting errors	HASSNER, MARTIN
<u>11059734</u>	Not Issued	30	02/16/2005	Techniques for performing Galois field logarithms for detecting error locations that require less storage space	HASSNER, MARTIN
<u>11154978</u>	Not Issued	20	06/15/2005	Error detection and correction for encoded data	HASSNER, MARTIN
<u>11207474</u>	Not Issued	30	08/18/2005	Decoding error correction codes using a modular single recursion implementation	HASSNER, MARTIN
<u>11341230</u>	Not Issued	25	01/26/2006	Combined encoder/syndrome generator with reduced delay	HASSNER, MARTIN
<u>07175171</u>	4949196	150	03/30/1988	METHOD AND APPARATUS FOR ASYMMETRICAL RLL CODING	HASSNER, MARTIN A.
<u>07200322</u>	4882583	150	05/31/1988	MODIFIED SLIDING BLOCK CODE FOR LIMITING ERROR PROPAGATION	HASSNER, MARTIN A.
<u>07284979</u>	4951284	250	12/14/1988	METHOD AND MEANS FOR CORRECTING RANDOM AND BURST ERRORS	HASSNER, MARTIN A.
<u>07493568</u>	4994995	150	03/14/1990	BIT-SERIAL DIVISION METHOD AND APPARATUS	HASSNER, MARTIN A.
<u>07917704</u>	Not Issued	166	07/20/1992	PROGRAMMABLE SEQUENTIAL SOLUTION OF KEY EQUATION FOR LINEAR ALGEBRAIC CODES	HASSNER, MARTIN A.
<u>08008922</u>	5444719	250	01/26/1993	ADJUSTABLE ERROR-CORRECTION COMPOSITE REED-SOLOMON ENCODER/SYNDROME GENERATOR	HASSNER, MARTIN A.
<u>08089211</u>	5384567	150	07/08/1993	COMBINATION PARALLEL/SERIAL EXECUTION OF SEQUENTIAL ALGORITHM FOR DATA COMPRESSION/DECOMPRESSION	HASSNER, MARTIN A.
<u>08247446</u>	5487077	150	05/23/1994	LOCATION DEPENDENT VARIABLE ERROR CORRECTION PROCESSING FOR MULTI-TRACK RECORDING MEDIA USING VARIABLE LENGTH	HASSNER, MARTIN A.

<u>10040115</u>	6903887	150	01/03/2002	MULTIPLE LEVEL (ML), INTEGRATED SECTOR FORMAT (ISF), ERROR CORRECTION CODE (ECC) ENCODING AND DECODING PROCESSES FOR DATA STORAGE OR COMMUNICATION DEVICES AND SYSTEMS	HASSNER, MARTIN AURELIANO
<u>10216094</u>	6842062	150	08/09/2002	INTEGRATED ELECTRONIC CIRCUIT INCLUDING NON-LINEAR INDUCTOR DEVICES	HASSNER, MARTIN AURELIANO
<u>10216562</u>	6777998	150	08/09/2002	INTEGRATED ELECTRONIC CIRCUIT INCLUDING NON-LINEAR DEVICES	HASSNER, MARTIN AURELIANO
<u>10217728</u>	Not Issued	94	08/12/2002	ALGEBRAIC DECODER AND METHOD FOR CORRECTING AN ARBITRARY MIXTURE OF BURST AND RANDOM ERRORS	HASSNER, MARTIN AURELIANO
<u>10291087</u>	6928515	150	11/09/2002	INTEGRATED SECTOR FORMAT-ERROR CORRECTION CODE SYSTEM AND METHOD FOR EFFICIENT WRITING IN A DISK ARRAY SYSTEM	HASSNER, MARTIN AURELIANO
<u>10301151</u>	6891690	150	11/20/2002	ON-DRIVE INTEGRATED SECTOR FORMAT RAID ERROR CORRECTION CODE SYSTEM AND METHOD	HASSNER, MARTIN AURELIANO
<u>10453550</u>	Not Issued	61	06/04/2003	Method for correcting a burst of errors plus random errors	HASSNER, MARTIN AURELIANO
<u>10640915</u>	7080200	150	08/14/2003	SYSTEM AND METHOD FOR HANDLING WRITES IN HDD USING 4K BLOCK SIZES	HASSNER, MARTIN AURELIANO
<u>10666401</u>	Not Issued	71	09/17/2003	Direct partial update of CRC/ECC check bytes	HASSNER, MARTIN AURELIANO
<u>10689814</u>	Not Issued	93	10/20/2003	GENERALIZED PARITY STRIPE DATA STORAGE ARRAY	HASSNER, MARTIN AURELIANO
<u>08896348</u>	6154868	150	07/18/1997	METHOD AND MEANS FOR COMPUTATIONALLY EFFICIENT ON-THE-FLY ERROR CORRECTION IN LINEAR CYCLIC CODES USING ULTRA-FAST ERROR LOCATION	HASSNER, MARTIN AURELIANO
<u>08987208</u>	5963152	150	12/09/1997	RESOLVING BLOCK METHOD FOR	HASSNER,

				SYNCHRONIZATION CORRECTION IN RUN-LENGTH LIMITED CODES	MARTIN AURELIANO
09090511	6141786	250	06/04/1998	METHOD AND APPARATUS FOR PERFORMING ARITHMETIC OPERATIONS ON GALOIS FIELDS AND THEIR EXTENSIONS	HASSNER, MARTIN AURELIANO
09115530	6195025	150	07/13/1998	METHOD AND MEANS FOR INVERTIBLY MAPPING BINARY SEQUENCES INTO RATE 2/3 (1,K) RUN-LENGTH-LIMITED CODED SEQUENCES WITH MAXIMUM TRANSITION DENSITY CONSTRAINTS	HASSNER, MARTIN AURELIANO
09124327	6233714	150	07/29/1998	GENERALIZED METHOD AND MEANS FOR DEFINING AND OPERATING A (D, K) PARTIAL- RESPONSE ML DETECTOR OF BINARY-CODED SEQUENCES	HASSNER, MARTIN AURELIANO

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